MST3113LAD3

30V_{DS}/±20V_{GS} Dual N-Channel MOSFET

General Description

These N-Channel enhancement mode power field effect transistors are using Trench technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

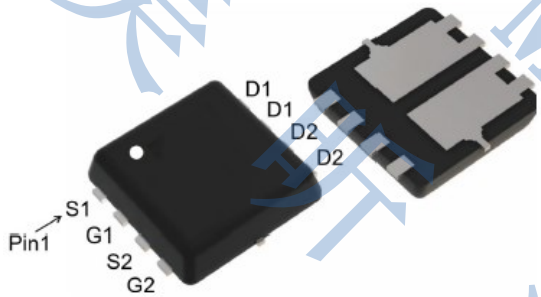
Features

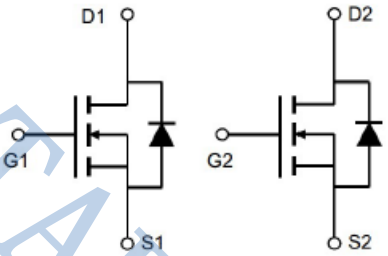
BVDSS	30V
$R_{DS(ON)}$(10V)	9.2mΩ（TYP）
$R_{DS(ON)}$(4.5V)	12mΩ（TYP）
I_D	30A

100% UIS TESTED!

100% ΔVds TESTED!

PDFN3030-Dual





Absolute maximum ratings（T_A=25℃）

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Continuous Drain Current (T_C=25℃)	30	A
	Continuous Drain Current (T_C=100℃)	20	A
I_{DM}	Pulesd Drain Current	90	A
I_{AS}	Avalanche Current (L=0.1mH)	23	A
E_{AS}	Single Pulsed Avalanche Energy	26.5	mJ
P_D	Maximum Power Dissipation (T_C =25℃)	24.5	W
	Maximum Power Dissipation (T_C =100℃)	9.8	W
T_J,T_{STG}	Operating,Storage Temperature Range	-55~150	℃

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JC}$	Thermal Resistance,Junction-to-Case	5.1	---	℃/ W
$R_{\theta JA}$	Thermal Resistance,Junction-to-Ambient	60	---	℃/ W

Electrical Characteristics(T_J=25℃,unless otherwise noted)

Static State Characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
B_{VDSS}	Drain-Source Breakdown Voltage	V_{GS}=0V,I_D=250μA	30	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=30V, V_{GS}=0V	---	---	1	μA
I_{GSS}	Gate -Source Leakage Current	V_{GS}=±20V, V_{DS}=0V	---	---	±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}= V_{GS},I_D=250μA	1.1	1.5	1.9	V
$R_{DS(ON)}$	Drain-Source On-stage Resistance	V_{GS}=10V,I_D=10A	---	9.2	12	mΩ
		V_{GS}=4.5V,I_D=4.5A	---	12	15	

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
C_{iss}	Input capacitance	V_{DS}=15V	---	498	---	pF
C_{oss}	Output capacitance	V_{GS}=0V	---	80	---	
C_{rss}	Reverse transfer capacitance	f=1MHz	---	73	---	
R_g	Gate Resistance	f=1MHz	---	4.5	---	Ω
Q_g	Total Gate Charge	V_{DS}=15V	---	13.4	---	nC
Q_{gs}	Gate Source Charge	V_{GS}=10V	---	1.4	---	
Q_{gd}	Gate Drain Charge	I_D=10A	---	3.5	---	
$t_{d(on)}$	Turn-on delay Time	V_{DS}=15V	---	5.1	---	ns
t_r	Rise time	V_{GS}=10V	---	2.0	---	
$t_{d(off)}$	Turn-off delay Time	R_G=3Ω	---	16	---	
t_f	Fall time	I_D=10A	---	4.5	---	
V_{SD}	Diode Forward Voltage	V_{GS}=0V,I_{SD}=10A	---	0.7	1.2	V
t_{rr}	Reverse Recovery Time	I_D=10A	---	6.5	---	ns
Q_{rr}	Reverse Recovery Charge	d_i/d_t=100A/μs	---	12.7	---	nC

Note:

1.Repetitive Rating : Pulsed width limited by maximum junction temperature.

2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.

3.Essentially independent of operating temperature.

Electrical Characteristics Diagrams

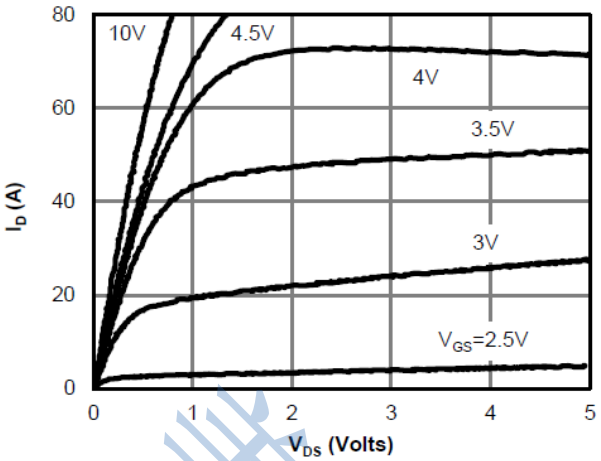


Figure 1. On-Region Characteristics

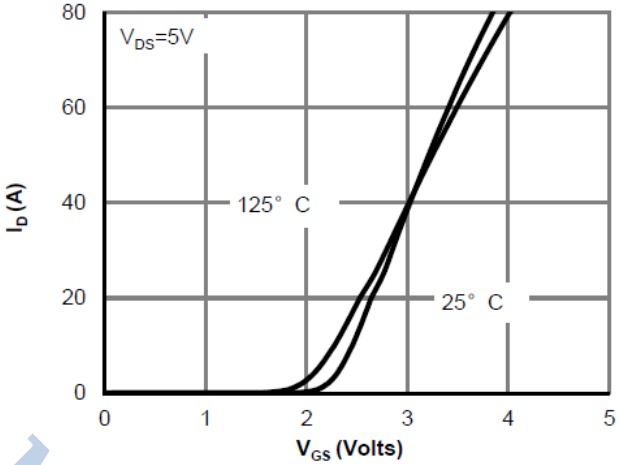


Figure 2. Transfer Characteristics

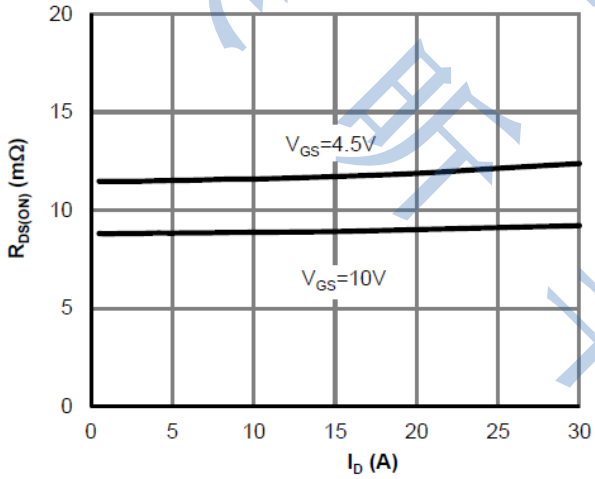


Figure 3. R_{DSON} vs. I_D and Gate Voltage

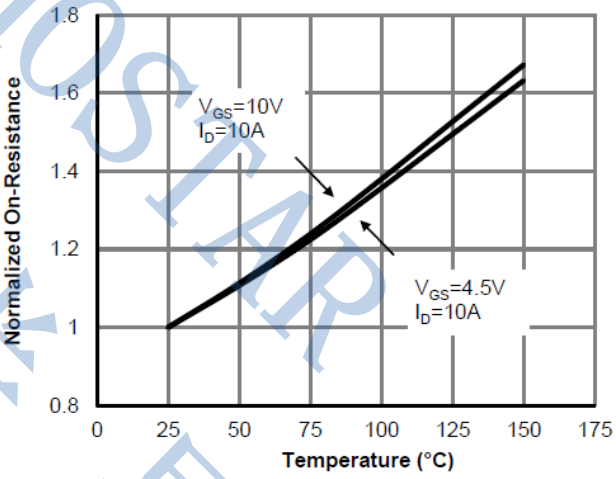


Figure 4. R_{DSON} vs. Junction Temperature

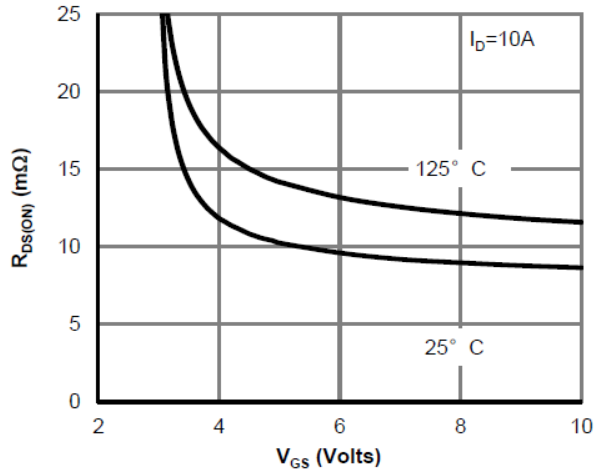


Figure 5. R_{DSON} vs. Gate Voltage

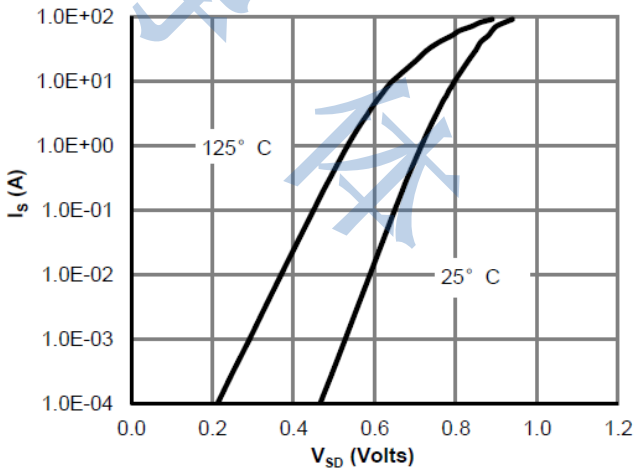


Figure 6. Body-Diode Characteristics

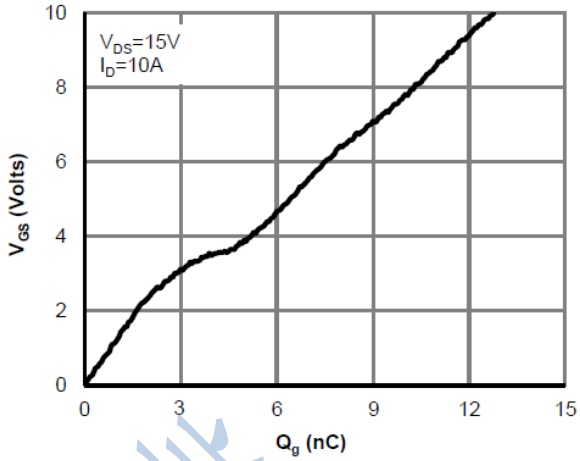


Figure 7. Gate-Charge Characteristics

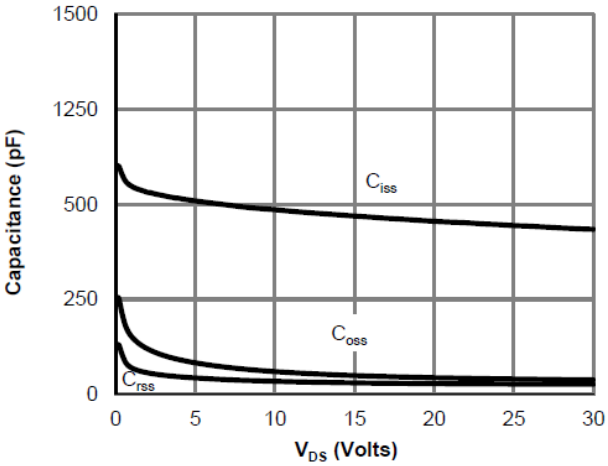


Figure 8. Capacitance Characteristics

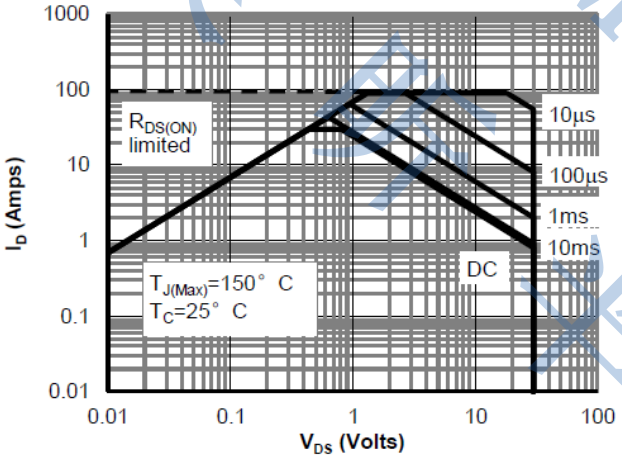


Figure 9. Safe Operating Area

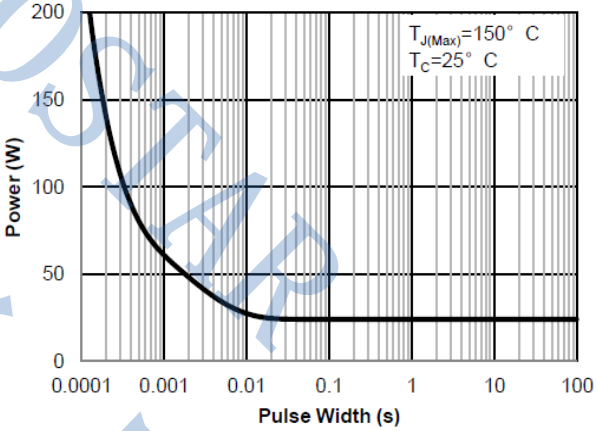
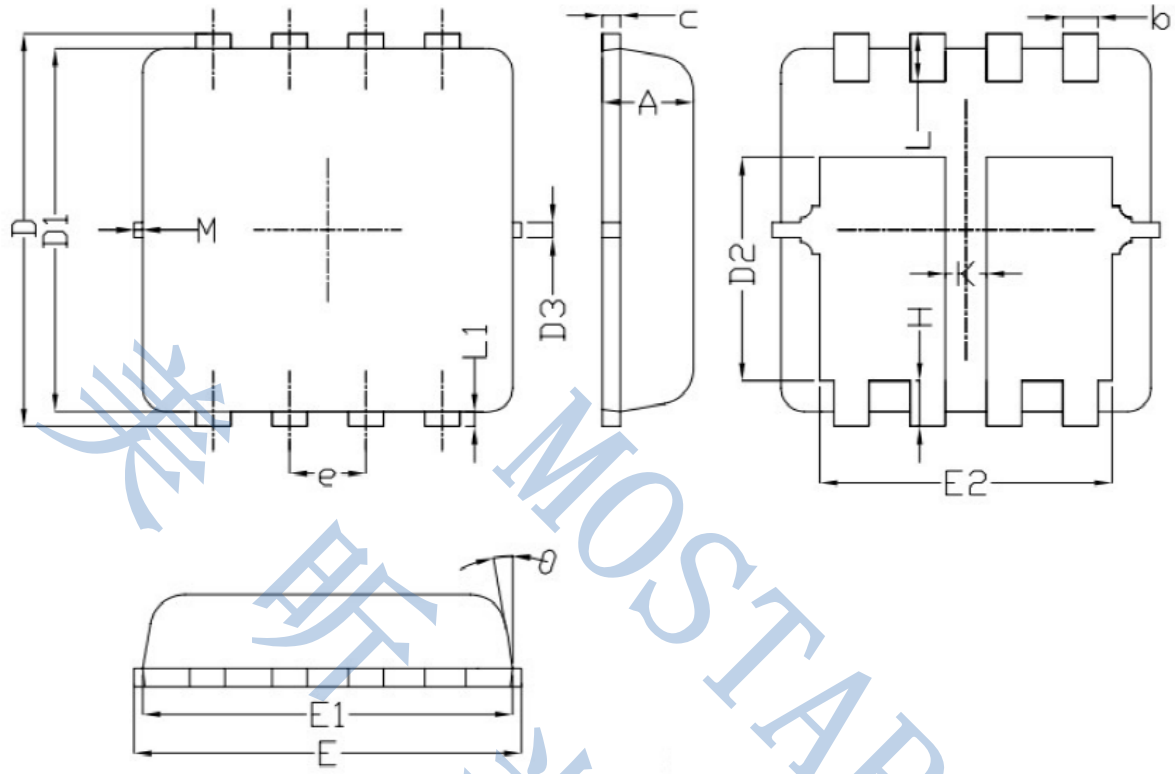


Figure 10. Single Pulse Power Rating Junction-to-Ambient

PDFN3030-Dual PACKAGE INFORMATION



Symbol	Dimensions In Millimeters(mm)	
	MIN	MAX
A	0.65	0.90
b	0.25	0.35
c	0.10	0.25
D	3.25	3.45
D1	3.00	3.20
D2	1.70	2.00
D3	0.13BSC	
E	3.20	3.40
E1	3.00	3.20
E2	2.30	2.60
e	0.65BSC	
H	0.30	0.50
L	0.30	0.50
L1	0.13BSC	
K	0.30	---
θ	0°	14°
M	---	0.15